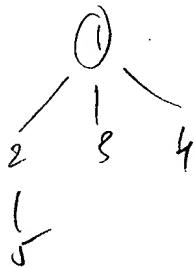


	L #	Hits	Search Text	DBs	Time Stamp
1	L5	4635	(two double dual first with second) adj3 (storage memory buffer queue bus sdram dma ram rom eprom eeprom+) with (card adapt\$2 nic board controller)	USPAT	2003/04/04 10:06
2	L6	1678	(usb "1394" ieee1394 firewire hotwire) with (cpu processor microprocessor chip control\$3 microcontroller)	USPAT	2003/04/04 10:10
3	L7	887	(usb "1394" ieee1394 firewire hotwire) with (cpu processor microprocessor chip control\$3 microcontroller) with (card adapt\$2 nic board controller)	USPAT	2003/04/04 10:11
4	L8	34	5 and 7	USPAT	2003/04/04 10:11
5	L9	20	5 and 7 and (double dual two second) adj3 (link interface connect\$3)	USPAT	2003/04/04 10:12



	L #	Hits	Search Text	DBs	Time Stamp
1	L3	40436	(two double dual second) adj3 (storage memory buffer queue bus) with (card adapt\$2 nic board controller device unit)	USPAT	2003/04/04 08:10
2	L4	3806	(two double dual second) adj3 (storage memory buffer queue bus) with (card adapt\$2 nic board controller) same (cpu processor microprocessor)	USPAT	2003/04/04 08:12
3	L5	189	(two double dual second) adj3 (storage memory buffer queue bus) with (card adapt\$2 nic board controller) same (cpu processor microprocessor) same (protocol tcp udp ipx sna atm isdn ip)	USPAT	2003/04/04 08:13
4	L6	162	(two double dual second) adj3 (storage memory buffer queue bus) with (card adapt\$2 nic board controller) same (cpu processor microprocessor) same (protocol usb "1394" iee1394)	USPAT	2003/04/04 08:17
5	L7	14	(two double dual second) adj3 (storage memory buffer queue bus) with (card adapt\$2 nic board controller) same (cpu processor microprocessor) same (usb "1394" iee1394)	USPAT	2003/04/04 08:18